## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.	10/624,716
Filing Date	
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	2812
Examiner	S. Geyer
Attorney's Docket No	
Title: Methods of Forming Semiconductor Constructions	

## INFORMATION DISCLOSURE STATEMENT

## PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

The attached Form PTO/SB/08a is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. No admission is made regarding whether the listed references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated:

By: [Je/my]

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